

1/4

FIG. 1

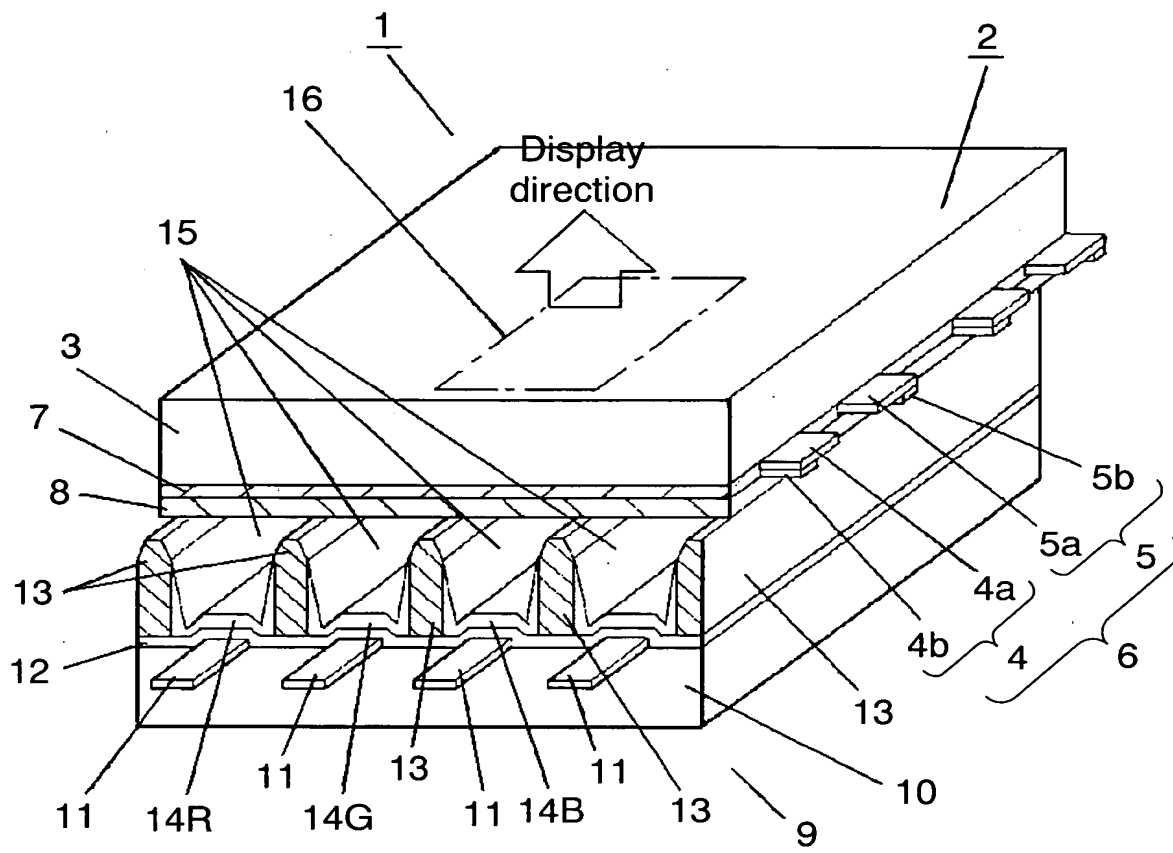
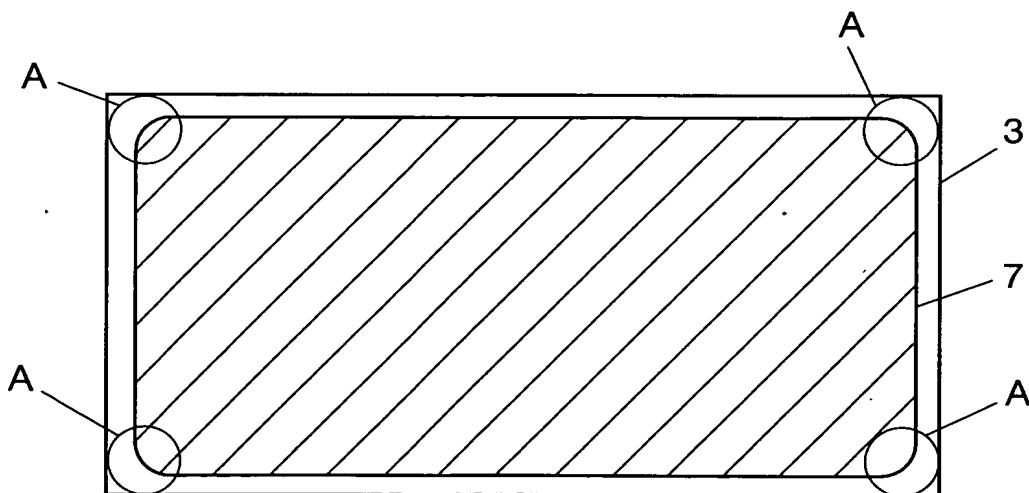


FIG. 2



2/4

FIG. 3

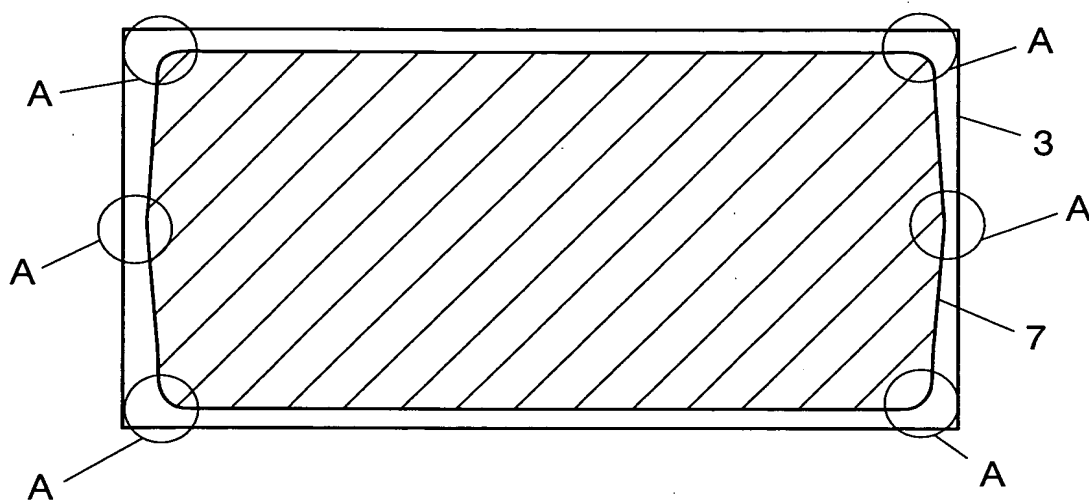
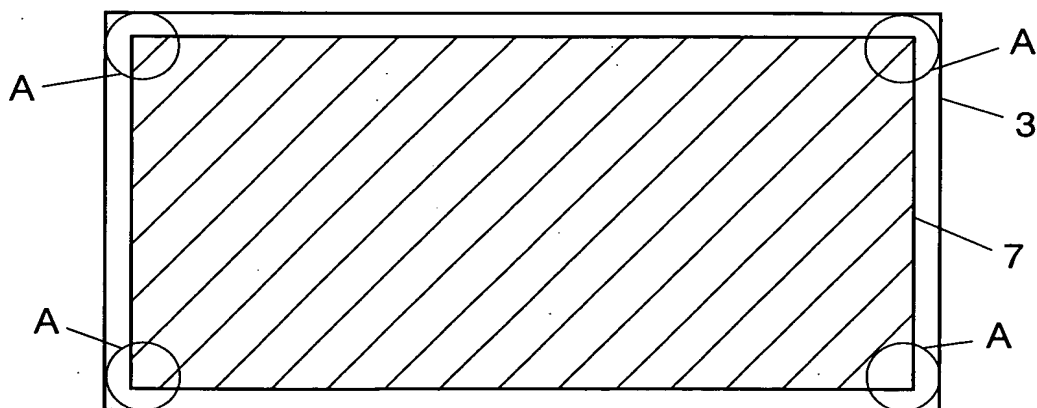


FIG. 4



3/4

FIG. 5A

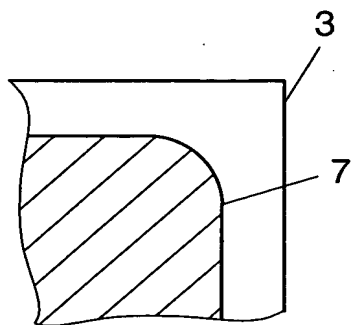


FIG. 5B

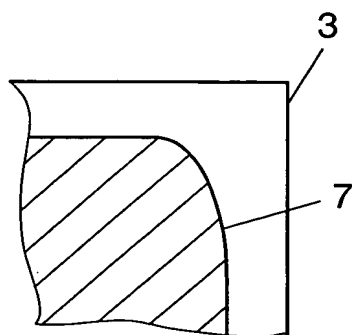
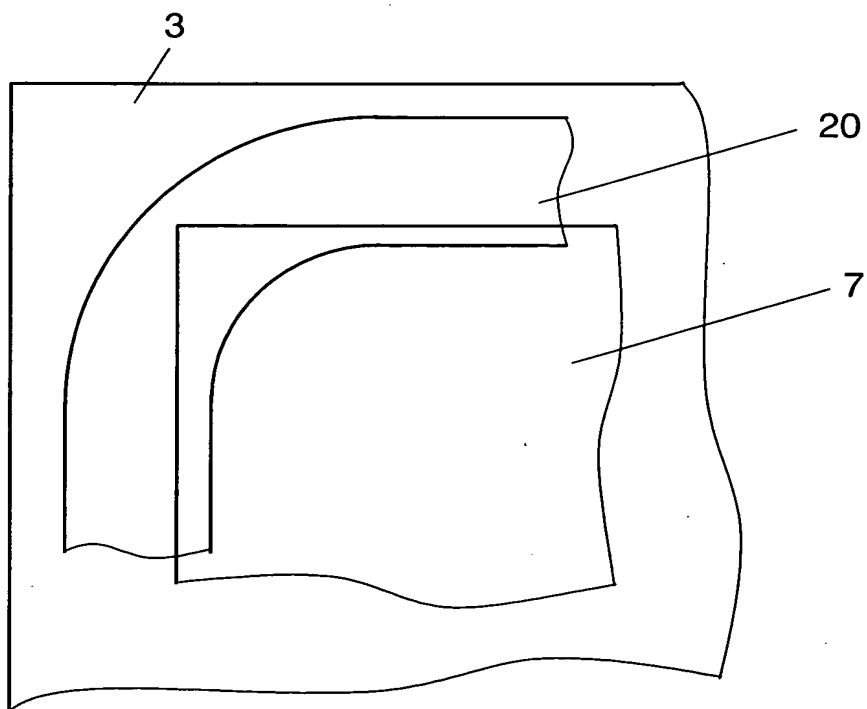


FIG. 6



Reference marks in the drawings

- 1 plasma display panel
- 2 front plate
- 3 front substrate
- 4 scan electrode
- 4a,5a transparent electrode
- 4b,5b bus electrode
- 5 sustain electrode
- 6 display electrode
- 7 first dielectric layer
- 8 protective layer
- 9 back plate
- 10 back substrate
- 11 data electrode
- 12 second dielectric layer
- 13 barrier rib
- 14R,14G,14B phosphor layer
- 15 discharge space
- 20 sealing member